

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of

KIMURA et al

Atty. Ref.: 1035-471

Serial No. Unknown

Group:

Filed: September 26, 2003

Examiner:

For: CHIP-STACK SEMICONDUCTOR DEVICE AND  
MANUFACTURING METHOD OF THE SAME

\* \* \* \* \*

September 26, 2003

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

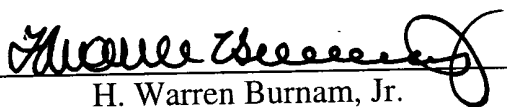
**INFORMATION DISCLOSURE STATEMENT**

As suggested by 37 C.F.R. 1.97, the undersigned attorney brings to the attention of the Patent and Trademark Office the references listed on the attached form PTO-1449, a copy of each of which is enclosed. This is not to be construed as a representation that a search has been made or that no better prior art exists, or that a reference is relevant merely because cited.

The Examiner is requested to initial the attached form PTO-1449 and to return a copy of the initialed document to the undersigned as an indication that the attached references have been considered and made of record.

Respectfully submitted,

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INFORMATION DISCLOSURE CITATION

ATTY. DOCKET NO.

SERIAL NO.

1035-471

Unknown

APPLICANT

KIMURA et al

(Use several sheets if necessary)

FILING DATE

GROUP

September 26, 2003

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,184,060	02/2001	SINIAGUINE			

FOREIGN PATENT DOCUMENTS

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO
223833/1998	04/1998	JAPAN			ABSTR.	
3186941	05/2001	JAPAN			ABSTR.	

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)


\*Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.